

Customer No.: 31561
Application No.: 10/708,666
Docket No.: 12423-US-PA-X-0P

AMENDMENT

Please amend the application as indicated hereafter.

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In the Claims :

1. (original) A memory device, comprising:

a plurality of isolation structures disposed in a substrate, defining a plurality of active regions in the substrate;

a plurality of pairs of word lines, substantially parallel to one another, disposed on and in a direction vertical to the plurality of the isolation structures and the plurality of the active regions, wherein the active regions that are covered by the plurality of pairs of the word lines are defined as a plurality of first channel regions;

a plurality of first gates disposed on the plurality of the first channel regions and between the substrate and the plurality of word lines;

a plurality of pairs of source lines, substantially parallel to the plurality of pairs of the word lines, each pair of source lines being between each pair of word lines, wherein the plurality of the source lines are disposed in a direction vertical to the plurality of the isolation structures and the plurality of the active regions, and wherein the active regions that are covered by the plurality of pairs of the source lines are defined as a plurality of second channel regions;

a plurality of second gates, in strip shape, disposed on and in a direction vertical to the plurality of the isolation structures and the plurality of the active regions and between the substrate and the plurality of the source lines;